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Abstract of the Disclosure

~~ABSTRACT~~

~~Computer-Supported Method For Partitioning An Electrical Circuit~~

A partition of an electrical circuit is formed in that the electrical circuit is imaged on a graph (102) and weighting values are allocated (103) to the edges of the graph. The weighting values describe a required calculating outlay for determining electrical descriptive quantities for the respective element of the electrical circuit represented by the edge. A check is carried in iterative methods to see whether, proceeding from preceding iteration steps edges grouped together [sic], [...] a sum of the weighting values of the edges [...] a sum value lies between a first threshold and a second threshold by adding a further edge. When this is the case, then a further check is carried out to see whether a plurality of terminals of the elements within the partition to elements outside the partition is increased by adding new edges. When this is not the case, then the respective edge is also incorporated into the partition.

Fig. 1